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Attorney Docket No. 1460.1044

## 医大块层域的TED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Toru KOGA

Application No.: 10/628,469

Confirmation No.: 4696

Filed: July 29, 2003

U.S. Patent No.: 6,831,870 B2

Issued: December 14, 2004

For: SEMICONDUCTOR MEMORY

**ATTENTION** 

CERTIFICATE OF CORRECTION

BRANCH

Batch No.:

Certificate

MAR 0 3 2005

of Correction

## REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

Patentee(s) respectfully request(s) that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake(s) shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date:

Ву

H. J. Sta

Registration No. 22,010

1201 New York Ave, N.W., Suite 700

tels: 28,2005.

Washington, D.C. 20005 Telephone: (202) 434-1500 Facsimile: (202) 434-1501

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Approved for use through 09/30/2000. OMB 0651-0033
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Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.
(Also Form PTO-1050)

## UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO : 6,831,870 B2 DATED: December 14, 2004 INVENTOR(S): Toru KOGA

Col. 11, line 25, change "comprises" to --comprising--.

Col. 12, line 26, change "units" to --unit--.

MAILING ADDRESS OF SENDER: STAAS & HALSEY LLP 1201 New York Ave., N.W. Suite 700 Washington, DC 20005

PATENT NO. 6,831,870

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